

Future Engineers' Leadership and Innovation Academy

23 November- 7 December 2018

Date	Time	Event	Venue
Thursday 22 November		Registration	Jiulonghu Hotel
Friday 23 November	08:30-11:40 11:40-13:00 14:00	UCEER Presidents' Forum 2018 Lunch Tour the Campus and labs of School of Transportation (SEU) Visit Wireless Valley	Holiday Inn Holiday Inn Jiulonghu Campus
Saturday 24 November <i>Social Activities</i>	10:00-12:00 12:00-13:00 14:00	Huawei Research Institute in Nanjing Lunch National Geopark of Mount Fang	
Sunday 25 November <i>Social Activities</i>	09:00 09:30-12:00 12:00-13:30 14:30	Check Out Nanjing Museum Sun Yat-sen Mausoleum Lunch Confucius Temple East Zhonghua Gate Historical Culture Block	Jiulonghu Hotel
Monday 26 November	09:00-17:00	Mini MBA – Leadership	Meeting room 1, Liuyuan Hotel
Tuesday 27 November	09:00-17:00	Mini MBA - Strategy and Markets	Meeting room 1, Liuyuan Hotel
Wednesday 28 November	09:00-17:00	Mini-MBA - Finance Essentials	Meeting room 2, Liuyuan Hotel
Thursday 29 November	09:00-17:00	Mini-MBA - Working in Global Teams	Meeting room 2, Liuyuan Hotel
Friday 30 November	09:00-17:00	Mini-MBA - Case Study	Meeting room 1, Liuyuan Hotel
Saturday 1 December <i>Social Activities</i>	09:00-11:30 11:30-12:30 13:30	Xuanwu Lake Park City Wall of Ming Dynasty Lunch Industrial Technology Research and Innovation Park Spreadtrum Communications, Inc CadenceDesign Systems, Inc	

Sunday 2 December Social Activities	09:00 11:00 11:30-13:30 13:30	Check out Check in Lunch Countryside tour-Bulao Village	Liu Yuan Hotel Kingsley Xinyitian Hotel
Monday 3 December	09:00-17:00	Design Workshop Prof. David Rooney	Room 1012, 10 th floor, ICisC
Tuesday 4 December	09:00-11:30 11:30-12:30 14:00-17:00 19:00-21:00	Introduction to Deep Learning Technology Prof. Qi Zhi/ Prof. Zhang Chuan, SEU Lunch FPGA Frontiers and Deep Learning Accelerator Design Dr Wang Xiaoqun, Xilinx Project Design and Discussion SEU/Xilinx/Huawei	Room 1012, 10 th floor, ICisC
Wednesday 5 December	09:00-11:30 11:30-12:30 14:00-17:00	Huaiwei Cloud FPGA Development Process Wang Yunfu, Senior Engineer from Huawei Lunch Group Work: Deep Learning Accelerator Design And Implementation Technical Support provided by Xilinx/Huawei	Room 1012, 10 th floor, ICisC
Thursday 6 December	09:00-11:30 11:30-12:30 14:00-17:00	Group Work: Deep Learning Accelerator Design and Implementation Technical Support provided by Xilinx/Huawei Lunch Group Work: Deep Learning Accelerator Design and Implementation Technical Support provided by Xilinx/Huawei	Room 1012, 10 th floor, ICisC
Friday 7 December	09:00-11:30 11:30-12:30	Showcase event and Closing Ceremony Lunch	Room 1007 10 th floor, ICisC
Saturday 8 December		Travel home	